

Pin	Label	FTDI	Type	Description
1	GND	GND	PWR	Signal ground
2	V+	VCC	PWR	Power supply +4.4 V to 5.25 V
3	NC			Not connected
4	D7	D7	I/O	Bidirectional data bus, bit 7
5	D6	D6	I/O	Bidirectional data bus, bit 6
6	D5	D5	I/O	Bidirectional data bus, bit 5
7	D4	D4	I/O	Bidirectional data bus, bit 4
8	D3	D3	I/O	Bidirectional data bus, bit 3
9	D2	D2	I/O	Bidirectional data bus, bit 2
10	D1	D1	I/O	Bidirectional data bus, bit 1
11	D0	D0	I/O	Bidirectional data bus, bit 0
12	GND	GND	PWR	Signal ground
13	GND	GND	PWR	Signal ground
14	ECS	EECS	I/O	EEPROM enable
15	ESK	EESK	I/O	EEPROM clock
16	EDT	EEDATA	I/O	EEPROM data I/O
17	NC			Not connected
18	3V3	3V3OUT	OUT	3.3 V output from internal voltage regulator
19	RXF#	RXF#	OUT	Data ready, if this signal is log.0, the data may be read by 1-0-1 sequence on RD# pin.
20	TXE#	TXE#	OUT	Transmit enabled, if this signal is log.0 the data may be written by 0-1-0 sequence on WR pin.
21	WR	WR	IN	Write signal – falling edge of this signal writes data to FTDI buffer.
22	RD#	RD#	IN	Read data – falling edge of this signal causes 1 byte of data to be read from FTDI buffer.
23	USBDP	USBDP	I/O	USB data signal plus
24	USBDM	USBDM	I/O	USB data signal minus

Signals with trailing '#' (hash mark) are active in log.0, other signals are active in log.1.

UMP1\_MANEN

## UMP1B - USB TO 8-BIT PARALLEL FIFO INTERFACE MODULE

UMP1B is a module based on FT245BM integrated circuit manufactured by FTDI Ltd., which provides with easy to use USB connectivity to PC without any need of additional knowledge about USB itself.

The module connects by 2 signals directly to USB connector, while the application side provides 8 bidirectional signals plus flow control. The module is capable of data transfer rates up to 1 MB/sec. In addition it supports external EEPROM memory to store VID and PID identifiers for OEM applications.

Software drivers for Windows 98/ME/2K/XP, Mac OS8/OS9/OS X and Linux are available for free on FTDI website (<http://www.ftdichip.com>).

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